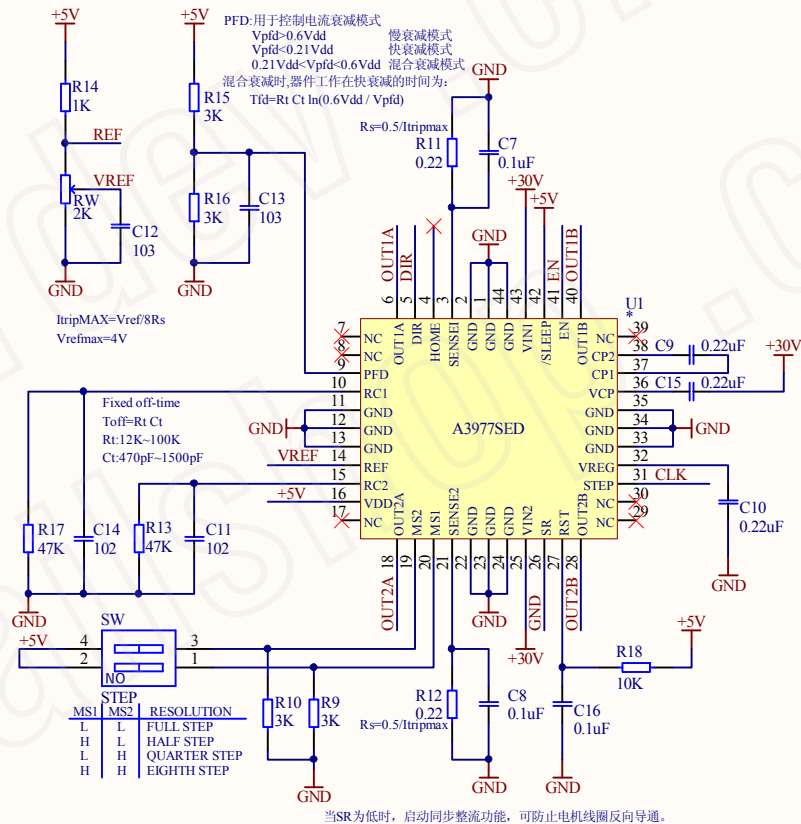


The diagram shows a 3-bit parallel-to-serial converter. It features three 4N26 optocouplers (U3, U4, U5) and a P521 component. The inputs are labeled J1, 9, 8, 7, 6, 5, 4, 3, 2, 1, and INPUT. The outputs are labeled CLK, DIR, and EN. Resistors R1 (510), R2 (510), R3 (1k), R4 (47K), R5 (47K), R6 (4K7), and R7 (3K) are used for signal conditioning and timing. The circuit is powered by +5V and GND.



|       |                                 |                 |               |           |       |
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| A4    |                                 | WWW.MAILSHOP.CN |               |           |       |
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